

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Lee A. Burton

Serial No. 10/618,041

Filed: July 11, 2003

For: SWITCH/NETWORK ADAPTER PORT INCORPORATING
SHARED MEMORY RESOURCES SELECTIVELY
ACCESSIBLE BY A DIRECT EXECUTION LOGIC ELEMENT
AND ONE OR MORE DENSE LOGIC DEVICES

Art Unit: 2182

Examiner: Sorrell, Eron J.

Confirmation No.: 4115

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Applicant has recently become aware of the additional references noted on the enclosed form PTO/SB/08A for the Examiner's consideration pursuant to the Applicant's duty of disclosure under 37 C.F.R. 1.97(b)(3). Because an office action has been mailed in this case, a check in the amount of \$180.00 is hereby enclosed to cover the fee believed required under 37 C.F.R. 1.17(p). Authorization is hereby granted to credit any overpayment or debit any underpayment of any fee required pursuant to this Supplemental Information Disclosure Statement to Deposit Account No. 50-1123.

Submission of the Supplemental Information Disclosure Statement is not a representation that a search has been made or that the cited information is, or is considered to be, material to patentability of the above application.

Respectfully submitted,

27 September 2006
Date


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